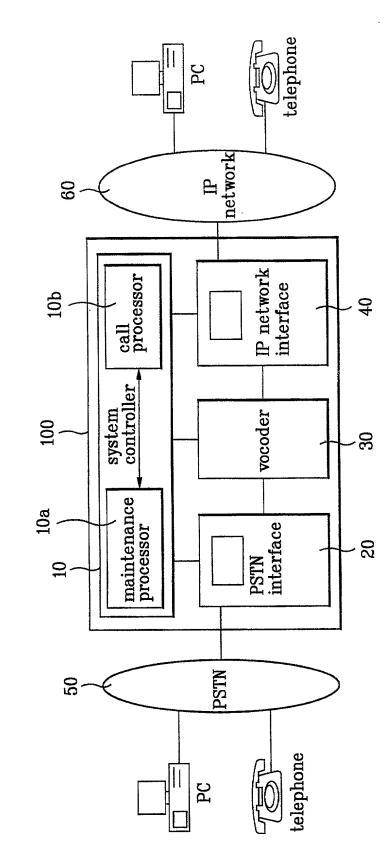
FIG. 1



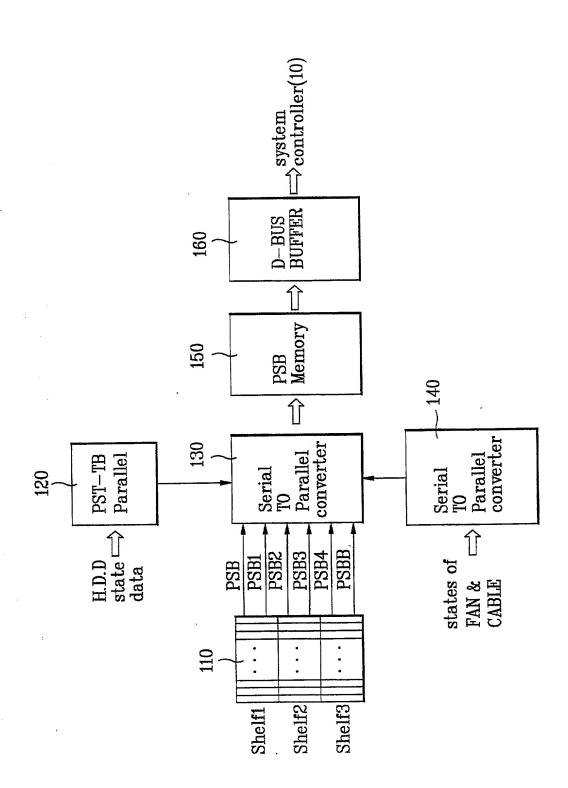


FIG. 3A

	Add	b7~b4	b3~b0	Remarks
shelf-	0003 0004 0005	DPM1 LC2	DPM1 LC1	DKU,PSU: PSB_TB
		LC4	LC3	conversion from
		LC6	LC5	PSB—TB to PSB
		LC8	LC7	,
		RDCC1	RDCC0	
			(HUBC)	
		DKU3,4	DKU1,2	
		PSU0,1	DKU5,6	
shelf~	8000	DPM2 LC2	DPM2 LC1	
	0009 000a 000b 000c 000d 000e 000f	LC4	LC3	
		LC6	LC5	
		LC8	LC7	
		RDCC1	RDCC0	
			(HUBC)	
	1	DKU3,4	DKU1,2	
		PSU0,1	DKU5,6	
	0010	DPM3 LC2	DPM3 LC1	
	0010 0011 0012	LC4	LC3	
	0013 0014 0015	LC6	LC5	
	0016 0017	LC8	LC7	
	0017	RDCC1	RDCC0	
			(HUBC)	
		DKU3,4	DKU1,2	
		PSU0,1	DKU5,6	

FIG. 3B

Add	b7~b4	b3~b0	Remarks
0018 0019	DPM4 LC2	DPM4 LC1	
000a	LC4	LC3	
000b 000c 000d	LC6	LC5	
000e	LC8	LC7	
000f	RDCC1	RDCC0	
		(HUBC)	
	DKU3,4	DKU1,2	
	PSU0,1	DKU5,6	
0020 0021	CPM NIC2	CPM NIC1	DKU,PSU: PSB_TB conversion from PSB—TB to PSB
0022	NIC4	NIC3	
0023	EIOC1	EIOC0	
0025 0026 0027			
0021	ASCC1	ASCCO	
	NSPC3	NSPC2	,
	RDCC1	RDCC0	
	PSU0,1	PSU1,2	

FIG. 4

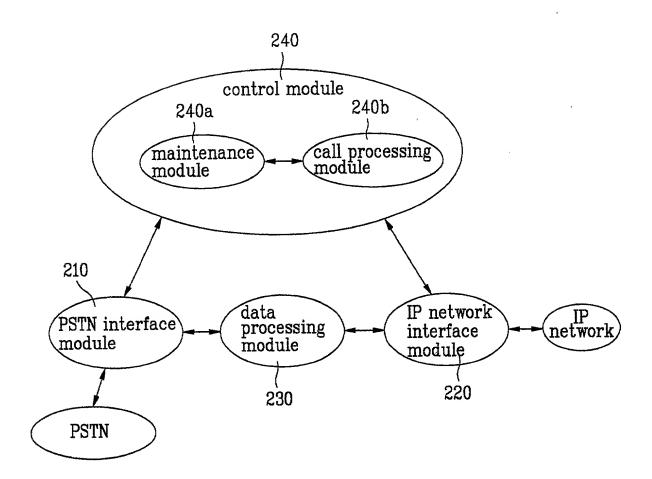


FIG. 5

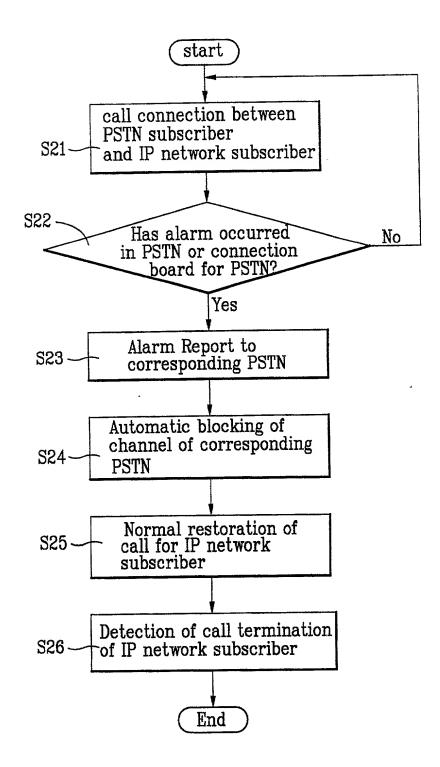


FIG. 6

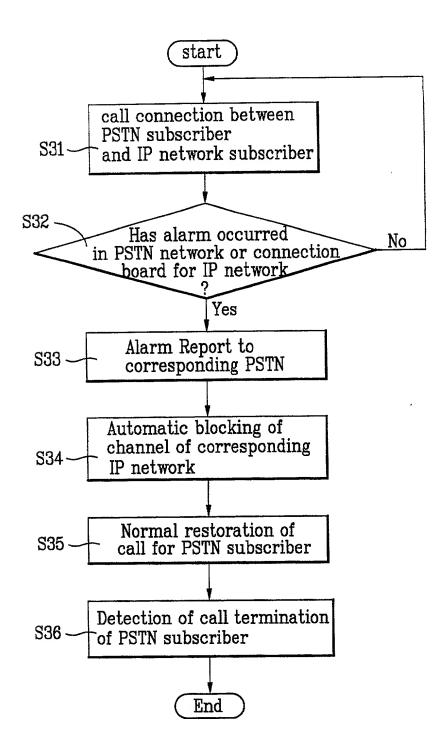


FIG. 7

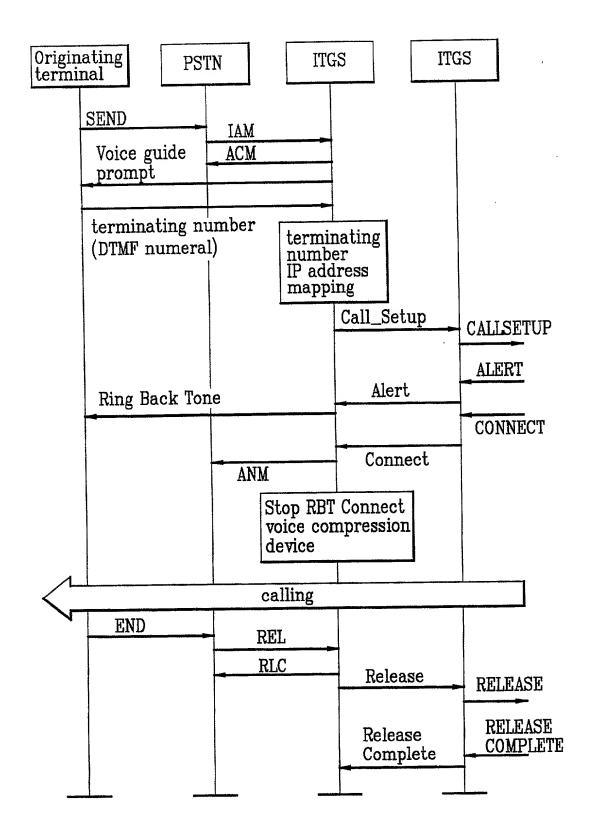


FIG. 8

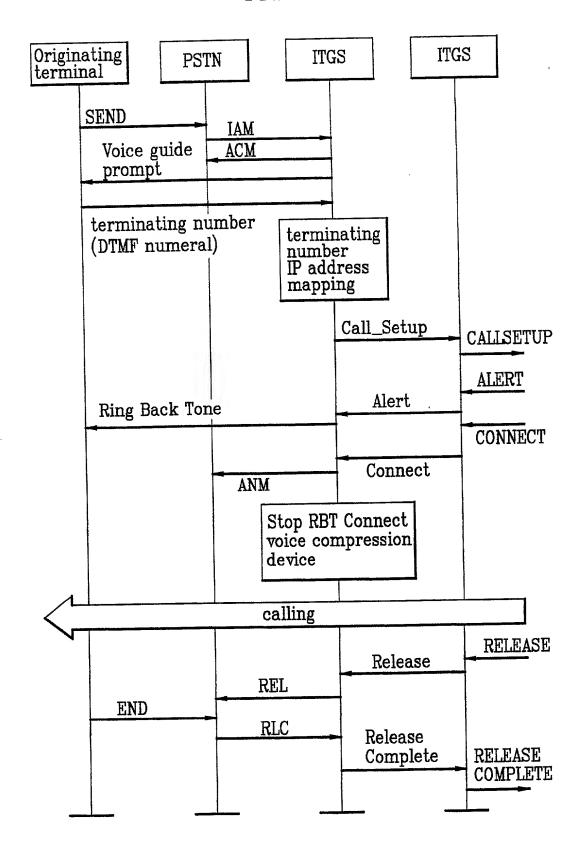


FIG. 9

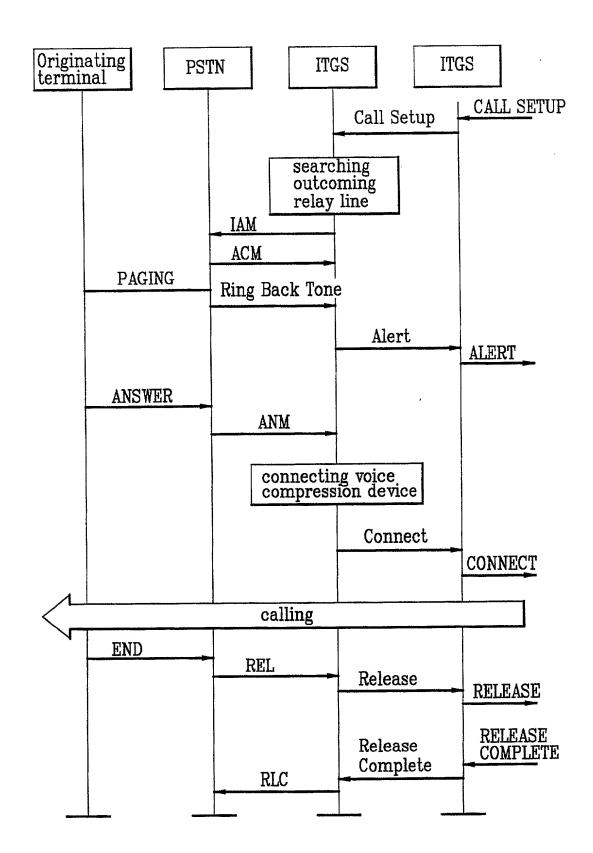


FIG. 10

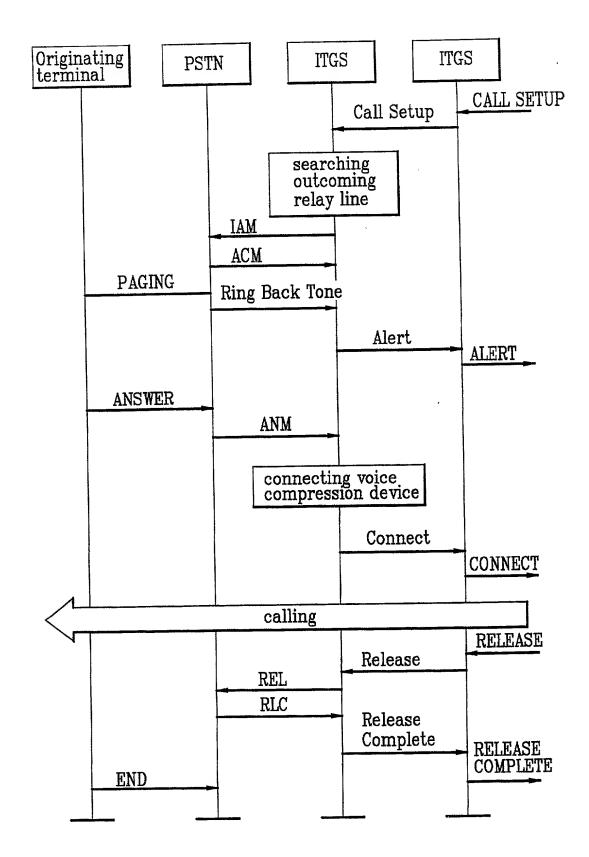


FIG. 11

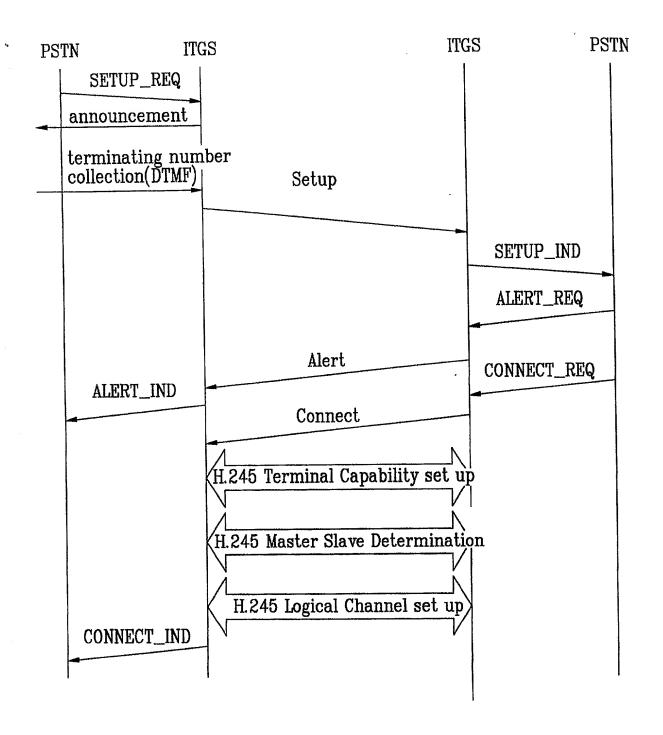


FIG. 12

